

## **General Description**

The BC7701 is a fully-integrated, single-chip Bluetooth Low Energy (BLE) controller with 2Mbps data rate supported. The BC7701 integrates a switch DC-DC regulator and designes to act as BLE slave according to the Bluetooth specification 5.2.

Moreover, during the intervals with no active BLE RF connection, the BC7701 works in Deep Sleep mode which can further reduce the power consumption.

The device is suitable for use in a wide range of BLE products such as health care products, home appliances, smart device information beacons, human interface device service products etc.

#### **Features**

#### Core

- · MCU integrated
- · 16MHz operating frequency
- The BC7701 implements on-chip DC-DC for a wide range of power and only one power supply is needed

### **On-chip Memory**

- 160KB on-chip Flash memory
- 20KB on-chip SRAM

#### **Bluetooth Low Energy Controller**

- 2.4GHz RF transceiver compatible with Bluetooth Low Energy (BLE) 5.2 specification
- 16MHz external crystal reference clock
- GFSK modulation, Frequency-Hopping Spread Spectrum (FHSS)
- · Support LE 1Mbps and 2Mbps
- Receiver supports programmable gain of over 70dB
- Excellent receiver sensitivity of -94dbm @1Mbps
- Programmable transmitter output power up to +3.5dBm
- Software-based True Random Number Generator (TRNG)
- Three operating modes: Normal, Deep-Sleep and Power-Down

#### Analog to Digital Converter – ADC

- 10-bit generic ADC
- Five external channels and one internal channel can be selected for ADC transfer
- Supports both single and continuous modes

#### I/O Ports - GPIO

• Up to 20 GPIO pins

There are up to 20 General Purpose I/O pins for the implementation of logic input/output functions.

#### Pulse Width Modulator - PWM

- 16-bit PWM timers
- 32kHz clock or 16MHz clock can be selected by PWM timers

The PWM consists of timer mode and PWM mode. The timer mode can generate interrupt to MCU and the PWM mode can generate PWM waveform for driving external device such as LED.

#### Watchdog Timer - WDT

• Reset event for the system

The system will reset after the watchdog timer is expired.

#### Real Time Clock - RTC

- 32-bit RTC timer
- · Wake-up MCU event

#### Inter-integrated Circuit - I<sup>2</sup>C

- Supports both master and slave modes with a frequency of up to 400kHz
- Supports 7-bit and 10-bit addressing modes and general call addressing

### Serial Peripheral Interface - SPI

- Supports both master and slave modes
- The SPI clock speed can be up to 4MHz

### Universal Asynchronous Receiver Transmitter – UART

- Full duplex communication
- Fully programmable serial communication characteristics including:
  - Word length: 5, 6, 7 or 8-bit character
  - Parity: Even, odd or no-parity bit generation and detection
  - Stop bit: 1 or 2 stop bit generation
- Error detection: Parity, overrun and frame error
- Auto hardware flow control mode RTS, CTS
- FIFO Depth: 8-level for both receiver and transmitter

The UART baud rate can be up to 3.2MHz.



### **Package and Operation Temperature**

- 32-pin QFN package
- Operation temperature range: -40°C to 85°C

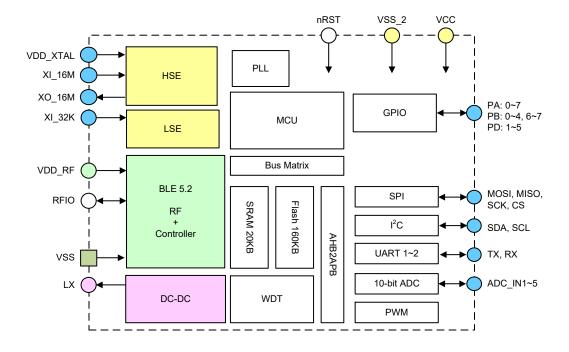
### **Overview**

### **Device Information**

	Peripherals	BC7701
Main Flash (KB)		160
SRAM (KB)		20
	PWM	6
Timers	WDT	1
	RTC	1
	SPI	1
Communication	UART	2
	I <sup>2</sup> C	1
	Resolution	10-bit
ADC	Speed	500K
	Channels	6
GPIO		Up to 20
CPU frequency		16MHz
Operating voltage		2.0V~3.6V
Operating temperature		-40°C~85°C
Package		32-pin QFN

**Features and Peripheral List** 

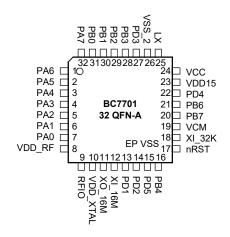
## **Block Diagram**



Rev. 1.00 2 September 29, 2021



# **Pin Assignment**



# **Pin Description**

Pin Name	Pin No.	Type <sup>(Note)</sup>	Description	
PA6	1	I/O	General purpose I/O	
PA5	2	I/O	General purpose I/O	
PA4	3	I/O	General purpose I/O	
PA3	4	I/O	General purpose I/O	
PA2	5	I/O	General purpose I/O	
PA1	6	I/O	General purpose I/O	
PA0	7	I/O	General purpose I/O	
VDD_RF	8	Р	RF power	
RFIO	9	AI/O	RF input or output	
VDD_XTAL	10	Р	Crystal oscillator power	
XO_16M	11	AO	16MHz Crystal oscillator output	
XI_16M	12	Al	16MHz Crystal oscillator input	
PD1	13	I/O	General purpose I/O	
PD2	14	I/O	General purpose I/O	
PD5	15	I/O	General purpose I/O	
PB4	16	I/O	General purpose I/O	
nRST	17	I	Hardware reset	
XI_32K	18	Al	32.768kHz Crystal oscillator input	
VCM	19	AO	Voltage for ADC reference. Use this pin only when ADC is enabled V <sub>CM</sub> =Common mode voltage	
PB7	20	I/O	General purpose I/O	
PB6	21	I/O	General purpose I/O	
PD4	22	I/O	General purpose I/O	
VDD15	23	Р	Internal power	
VCC	24	Р	Digital power supply	
LX	25	AO	Switching output. Connect this pin to the switching end of the inductor	
VSS_2	26	Р	Ground reference for digital I/O	
PD3	27	I/O	General purpose I/O	
PB3	28	I/O	General purpose I/O	
PB2	29	I/O	General purpose I/O	
PB1	30	I/O	General purpose I/O	

Rev. 1.00 3 September 29, 2021



Pin Name	Pin No.	Type <sup>(Note)</sup>	Description
PB0	31	I/O	General purpose I/O
PA7	32	I/O	General purpose I/O
VSS	EP	Р	Exposed Pad on the bottom of the package. Internally connected to RF Grounding. Solder this exposed pad to a PCB pad that uses multiple ground vias to provide heat transfer out of the device into the PCB ground planes. These multiple ground vias are also required to achieve the noted RF performance.

Note: I = Input; O = Output; A = Analog port; P = Power supply; EP = Exposed pad.

# **GPIO Mapping**

Pin Name	Pin No.	Туре	Description
PA0	7	I/O	UART1_TX
PA1	6	I/O	UART1_RX
PA2	5	I/O	I <sup>2</sup> C_SCL
PA3	4	I/O	I <sup>2</sup> C_SDA
PA4	3	I/O	SPI_SCK
PA5	2	I/O	SPI_MOSI
PA6	1	I/O	SPI_MISO
PA7	32	I/O	SPI_CS
PB0	31	I/O	PWM0
PB1	30	I/O	PWM1
PB2	29	I/O	PWM2
PB3	28	I/O	PWM3
PB4	16	I/O	PWM4
PB6	21	I/O	UART2_TX
PB7	20	I/O	UART2_RX
PD1	13	I/O	ADC Channel 1
PD2	14	I/O	ADC Channel 2
PD3	27	I/O	ADC Channel 3
PD4	22	I/O	ADC Channel 4
PD5	15	I/O	ADC Channel 5

## **Electrical Characteristics**

### **Absolute Maximum Ratings**

The following table shows the absolute maximum ratings of the device. These are stress ratings only. Stresses beyond absolute maximum ratings may cause permanent damage to the device. Note that the device is not guaranteed to operate properly at the maximum ratings. Exposure to the absolute maximum rating conditions for extended periods may affect device reliability.

Symbol	Parameter	Min.	Max.	Unit
Vcc	External Main Supply Voltage	2.0	3.6	V
Vin	Input Voltage on I/O	Vcc-0.3	Vcc+0.3	V
TA	Ambient Operating Temperature Range	-40	+85	°C
T <sub>STG</sub>	Storage Temperature Range	-55	+150	°C

Rev. 1.00 4 September 29, 2021



# **Recommended DC Operating Conditions**

T<sub>A</sub>=25°C

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Vcc	Operating Voltage	_	2.0	3.0	3.6	V

## A.C. Characteristics

T<sub>A</sub>=25°C

Symbol		Parameter	Min.	Тур.	Max.	Unit
Crystal Oscillato	or 16MHz		'	1		
f <sub>HSE</sub>	Frequency			16	_	MHz
ACC <sub>HSE</sub>	Frequency Accurac	cy Requirement	-30	_	30	ppm
ESR	Equivalent Series I	Resistance	_	_	80	Ω
C0	Crystal Shunt Capa	acitance	_	_	3	pF
CL	Crystal Load Capa	citance		7	_	pF
Crystal Oscillato	or 32.768kHz					
f <sub>LSE</sub>	Frequency		_	32.768	_	kHz
ACC <sub>LSE</sub>	Frequency Accurac	cy Requirement	-20	_	20	ppm
ESR	Equivalent Series I	Resistance		_	70k	Ω
C0	Crystal Shunt Capa	acitance	_	_	2	pF
CL	Crystal Load Capa	citance		12.5	_	pF
RX Characterist	ic		·			
CI0		Co-channel Interference	_	7	_	dB
CI1		Interfere at f <sub>OFFS</sub> =±1MHz	-9	_	-6	dB
CI2		Interfere at f <sub>OFFS</sub> =±2MHz	_	-44	_	dB
CI3	In-band Blocking	Interfere at f <sub>OFFS</sub> =±3MHz	_	-50	_	dB
CI4		Interfere at f <sub>IMAGE</sub>	_	-25	_	dB
CI5		Interfere at f <sub>IMAGE</sub> =±1MHz		-35	_	dB
Intermodulation	Pin=-64dBm; Punv f2-f1=3/4/5MHz	vant=50dBm; f0=2f1-f2,	-25	_	-22	dBm
PSENS	Sensitivity @ 1Mbp	Sensitivity @ 1Mbps		-94	_	dBm
PSENS	Sensitivity @ 2Mbp	os	_	-91	_	dBm
PTX	Output Power		_	3.5	_	dBm
TX Characteristi	ic					
P <sub>TX</sub>	Output Power			3.5	_	dBm
P <sub>BW</sub>	Modulation 20dB Bandwidth		_	_	1	MHz
PRF1	Out of Band Emission 2MHz		_	-20	_	dB
PRF2	Out of Band Emiss	ion 3MHz	_	-58	_	dB
Dev	Transmit FM Devia	tion	115	250	300	kHz
Drift	Transmit Drift in an	y Position		_	400	Hz/µs

Rev. 1.00 5 September 29, 2021



# **BLE Power Consumption Characteristics**

V<sub>CC</sub>=3V, T<sub>A</sub>=25°C

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I <sub>RX</sub>	Supply Current (RX Mode)	_	_	8	_	mA
I <sub>TX</sub>	Supply Current (TX Mode, 0dBm Output Power)	_	_	5.83	_	mA
I <sub>SLEEP</sub>	Supply Current (IDLE Mode when MCU Deep Sleep)	_	_	1.6	_	μA
I <sub>ACT</sub>	Supply Current (IDLE Mode when MCU Active)	_	_	1.38	_	mA
I <sub>PDN</sub>	Supply Current (Power Down)	_	_	1	_	μA

## I/O Port Characteristics

 $T_A=25^{\circ}C$ 

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
VIL	Low Level Input Voltage	3V I/O	0	_	0.3	V
V <sub>IH</sub>	High Level Input Voltage	3V I/O	2.7	_	3.3	V
Vol	Low Level Output Voltage	3V, I <sub>OH</sub> =0.25mA	_	_	0.3	V
V <sub>OH</sub>	High Level Output Voltage	3V, I <sub>OH</sub> =-0.25mA	V <sub>CC</sub> -0.3	_	Vcc	V

Rev. 1.00 6 September 29, 2021



# **Package Information**

Note that the package information provided here is for consultation purposes only. As this information may be updated at regular intervals users are reminded to consult the <u>Holtek website</u> for the latest version of the <u>Package/Carton Information</u>.

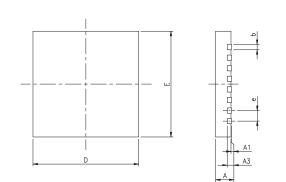
Additional supplementary information with regard to packaging is listed below. Click on the relevant section to be transferred to the relevant website page.

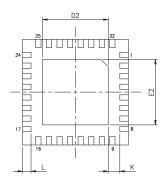
- Package Information (include Outline Dimensions, Product Tape and Reel Specifications)
- Packing Meterials Information
- Carton information

Rev. 1.00 7 September 29, 2021



# SAW Type 32-pin QFN (4mm×4mm×0.55mm) Outline Dimensions





Cumbal	Dimensions in inch					
Symbol	Min.	Nom.	Max.			
A	0.020	0.022	0.024			
A1	0.000	0.001	0.002			
A3	_	0.006 BSC	_			
b	0.006	0.008	0.010			
D	_	0.157 BSC	_			
E	_	0.157 BSC	_			
е	_	0.016 BSC	_			
D2	0.104	0.106	0.108			
E2	0.104	0.106	0.108			
L	0.010	0.012	0.014			
K	0.008	_	_			

Cumbal	Dimensions in mm					
Symbol	Min.	Nom.	Max.			
A	0.50	0.55	0.60			
A1	0.00	0.02	0.05			
A3	_	0.150 BSC	_			
b	0.15	0.20	0.25			
D	_	4.00 BSC	_			
E	_	4.00 BSC	_			
е	_	0.40 BSC	_			
D2	2.65	2.70	2.75			
E2	2.65	2.70	2.75			
L	0.25	0.30	0.35			
K	0.20	_	_			

Rev. 1.00 8 September 29, 2021



### Copyright<sup>©</sup> 2021 by HOLTEK SEMICONDUCTOR INC.

The information appearing in this Data Sheet is believed to be accurate at the time of publication. However, Holtek assumes no responsibility arising from the use of the specifications described. The applications mentioned herein are used solely for the purpose of illustration and Holtek makes no warranty or representation that such applications will be suitable without further modification, nor recommends the use of its products for application that may present a risk to human life due to malfunction or otherwise. Holtek's products are not authorized for use as critical components in life support devices or systems. Holtek reserves the right to alter its products without prior notification. For the most up-to-date information, please visit our web site at http://www.holtek.com.

Rev. 1.00 9 September 29, 2021